

Objective: A position to engineer high performance, high quality system products

Education: 1984 Baccalaureate of Science in Electrical Engineering
1986 Master of Science in Electrical Engineering (Thesis in VLSI Computer Aided Design)
University of Rochester, Dept. of Elec. Eng., Rochester, NY 14627

Experience:

Jan. 2004—present **System Engineering Consultant**
Chronolytics Inc., 50 Gorham St., Canandaigua, NY 14424

Oct. 1993—Dec. 2003 **System Engineering Consultant**
Embedded Real-Time Inc., 250 Turk Hill Park, Fairport, NY 14450

Oct. 1993—Dec. 1999 **System Engineering Consultant**
Dot4 Inc., 6080 Jericho Turnpike, Suite 211, Commack, New York 11725

Apr. 2009 – present Designed, coded and tested firmware for mobile computing device (MID) based on Samsung s3c6410. Principle author of accelerated Xwindows driver for SoC 2d accelerator. Used lzma to reduce install image size by 25%. For more detail, see <http://chronolytics.com/mid.html>

Oct 2005—Mar. 2009 Designed and coded software defined radio operating environment for **Harris RF**. Areas of technical specialty were reliability improvement, Hardware Abstraction Layer (HAL) and performance analysis. Large code base written in C++/CORBA RPC/QNX RTOS. Wrote several device drivers for flash/EEPROM devices. Software lead on port to ARM MX27 processor. Member of small team that did targeted re-factor to improve security, performance and maintainability.

Apr. 2005—Oct. 2005 Design, code and test of real-time reactive tasking **Chronolytics** application environment within Linux. System includes non-volatile storage, event observation, generalized asynchronous I/O, reference counted objects, high resolution timers, kernel level event tracing, dynamic loaded modules.

Jul. 1999—Mar. 2005 Provide systems knowledge for large (1000KSLOC) code base for large **Xerox** digital imaging/printing projects. Specialized in system performance/problem solving for which broad systems background is required. Code base requires fluency in C++/VxWorks/Solaris/NT. Developed methods for analyzing systems to determine causation and prototyped/developed corrective actions for performance/reliability failures.

Oct. 1993--Jul. 1999 Provide OS knowledge (LynxOS SPARC/PPC/X86) and system engineering expertise for large (3000KSLOC) code base. Responsible for driving requirements and specifications, reliability and quality assurance, and integration for large networked, image processing technology for **Xerox**. Involved with day-to-day operations, coding, scheduling, and planning. Participated in architecture and software delivery process. Specialized in system performance/problem solving for which broad systems background is required.

Apr. 1990--Sept. 1993 **Senior Systems Engineer**
Concurrent Computer Corp., 635 Cross Keys Park, Fairport, NY 14450
Provide system level consultation for Masscomp real-time UNIX multi-processor computer systems
Worked with customers on all aspects of UNIX and real-time system problems
Designed a suite of real-time system analysis tools
Developed and taught a course on Real-Time System Engineering for Open Systems

Jul. 1986--Mar. 1990 **Software Manager**
Micropen Inc., 3800 Monroe Ave., Pittsford, NY 14534
Primary software architect and development manager for multi-disciplinary CAD/CAM capital equipment manufacturer
Responsible for all aspects of software CAD development and maintenance, QA/testing, real-time machine control software development, managing engineering computer facilities and computer related production

References: Available upon request